



# JIEEEEC 2005

## The 6<sup>th</sup> Jordanian International Electrical & Electronics Engineering Conference

November 15-17, 2005, Amman - Jordan

### CALL FOR PAPERS



#### Organizing Committee

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#### Workshops

Pre-conference workshops will be held.

#### Working Language

English is the working language of the conference.

JIEEEEC05 is Organized by Jordan Engineers Association (JEA) in cooperation with The Institution of Electrical Engineers (IEE – Jordan) and The Institute of Electrical & Electronics Engineers (IEEE - Jordan section).

Following the success of five previous JIEEEECs, Jordan Engineers Association will hold its sixth international JIEEEEC conference. The conference will provide a forum for researchers, experts and engineers to discuss research ideas, exchange knowledge and practices, and review advancements in related topics. Areas of interest include, but not limited to:

#### Symposium on Communications & Signal Processing

- Satellite Communications.
- Wireless Communications.
- Modulation & Coding.
- Antenna Technology & Propagation.
- Protocols & Data Communications.
- Optical Communications.
- Spread Spectrum Communications.
- Networking.
- Digital Signal Processing.
- Wavelet Analysis

#### Symposium on Biomedical Engineering

- Biomedical Signal Processing.
- Biomedical Imaging and Archiving.
- Speech Processing.
- Physiological Measurement.
- Cardiovascular Systems.

#### Symposium on Power & Machines

- Power Systems Operation & Control.
- Electrical Machines.
- Power Quality
- Electromagnetic Compatibility.
- Power System Interconnection.
- Power Systems Analysis.
- Deregulation of Power Systems.
- Reliability

#### Symposium on Automation & Drives

- Conventional, Robust, Adaptive, & Optimal Control.
- System Identification.
- Process Control & Instrumentation.
- Power Electronics.
- Fuzzy Logic, Neural Networks & Intelligent Control.
- Electrical Drive Systems.
- Computer Control.
- Robotics.

#### Symposium on Computer Engineering

- Networking.
- System Software.
- Real-Time and Embedded Systems.
- Machine Intelligence & Computer Graphics.
- Computer Organization & Architecture.
- Evolutionary programming & Genetic Algorithms.
- Parallel and Distributed Systems.
- Software Engineering.
- Data warehousing & mining.
- Performance, Fault Tolerance, Reliability & Security.
- Pattern Recognition.
- Swarm Intelligence.
- e-learning

#### Symposium on Electronics

- Microelectronics
- Optoelectronics & Semiconductors
- VLSI
- Nanotechnologies

#### Deadlines

Submission of Papers	March 31, 2005
Notification of Acceptance	Aug. 31, 2005
Final Manuscripts are due by	Oct 1, 2005

#### Paper Submission

Prospective authors are strongly encouraged to submit 5 pages paper (A4 size) by due date electronically (e-mail: [chairjieeeec@jeaconf.org](mailto:chairjieeeec@jeaconf.org)) in pdf/doc format. Please provide keywords to indicate the area of the paper, and specify the area from the listed topics. Include contact information of corresponding author.

Further information available at:

[www.jeaconf.org](http://www.jeaconf.org)

#### Exhibition

An exhibition will be arranged beside the conference. The concerned companies can contact the exhibition's committee.

#### Technical Program Committee

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